

## CLAIMS

1. A telecommunications receiver, comprising:
  - means for associating categories with bit indices of a broadcast indicator bit stream;
  - means for waking up on a periodic basis to receive one or more bits in the broadcast indicator bit stream located at said bit indices;
  - means for receiving said one or more bits;
  - means for comparing each of said one or more bits to an on or off value; and
  - means for determining from said comparison whether or not to subsequently receive and decode an encoded bit stream that follows said broadcast indicator bit stream by a pre-determined time interval.
2. The receiver of claim 1, wherein the means for associating categories comprises means for hashing desirable categories of broadcast messages via a pre-determined hashing function to determine said bit indices.
3. The receiver of claim 1, wherein the means for receiving said one or more bits comprises means for demodulating an uncoded, on-off keying modulated direct sequence spread spectrum signal.
4. The receiver of claim 1, further comprising means for receiving one or more incoming page indicator bits on a same channel as that on which said one or more bits are received after receiving said one or more bits.
5. The receiver of claim 4, wherein the means for receiving said one or more bits comprises means for demodulating a first signal on a modified quick paging channel.
6. The receiver of claim 5, wherein the means for receiving one or more incoming page indicator bits comprises means for demodulating a second signal on said modified quick paging channel after demodulating said first signal.

7. The receiver of claim 4, wherein the means for receiving said one or more bits comprises means for receiving said one or more bits and said one or more incoming page indicator bits in an overlaid fashion on a modified quick paging channel.
8. The receiver of claim 1, wherein the means for receiving said one or more bits comprises means for demodulating a first signal on an auxiliary channel that is orthogonal to a quick paging channel.
9. The receiver of claim 8, further comprising means for receiving one or more incoming page indicator bits on the quick paging channel, wherein the means for receiving one or more incoming page indicator bits comprises means for demodulating a second signal on said quick paging channel.